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Intel Corporation

(Authorized Signature)

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dudi Amir

Title: METHODS FOR MANUFACTURING PRINTED CIRCUIT BOARDS USING A PARTIAL

PRINTING PROCESS AND APPARATUS

Docket No.: 884.147US2

Filed: September 8, 2003

Examiner: Ren Yan

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 10/657,681

Due Date: October 19, 2004

Group Art Unit: 2854

Confirmation No.: 1230

Notice of Allowance Date:

July 19, 2004

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X Communication Concerning Related Application(s) (1 pg.)

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

icant: Serial No.: Dudi Amir

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10/657,681

Group Art Unit: 2854

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METHODS FOR MANUFACTURING PRINTED CIRCUIT BOARDS USING

A PARTIAL PRINTING PROCESS AND APPARATUS

Assignee:

Intel Corporation

Customer No.: 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. 09/434944 6626099	Filing Date November 5, 1999	Attorney Docket 884.147US1	Title PARTIAL PRINTING PROCESS AND APPARATUS
10/657680	September 8, 2003	884.147US3	BLOCKING CAP FOR USE IN A PARTIAL PRINTING PROCESS AND APPARATUS

Respectfully submitted, **DUDI AMIR**

By Applicant's Representatives, SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Name